

IET Computers & Digital Techniques Call for Papers



SPECIAL ISSUE ON: Three-dimensional Integrated Circuits Design

3D Integration emerges as an attractive option to sustain Moore's law as well as to enable More-than-Moore. The key benefits of 3D ICs over a traditional two dimensional chip includes the reduction of global interconnects, higher packing density and smaller footprint, and enables mixed-technology integration. Design and test techniques and methodologies for 3D designs are imperative to realise 3D integration; novel architectures and design space exploration at the architectural level are also essential to leverage 3D integration technologies for performance gain.

IET Computers & Digital Techniques seeks original manuscripts for a Special Issue on 3D IC Design, scheduled for publication in September 2010. The topics of interest include, but are not limited to:

- novel 3D architectural level design
- design automation tools and algorithms for 3D IC design.
- thermal analysis methods and thermal-aware design techniques for 3D ICs
- cost and yield analysis for 3D ICs
- testing techniques for 3D ICs
- novel application enabled by 3D ICs

Prospective authors should follow the submission guidelines for *IET Computers & Digital Techniques* available at <http://www.ietdl.org/IET-CDT>. All papers must be submitted through the journal's Manuscript Central system: <http://mc.manuscriptcentral.com/iet-cdt>

Please indicate that you are submitting your article to the Special Issue on "Three-dimensional Integrated Circuits Design." All papers will undergo *IET Computers & Digital Techniques* rigorous peer review process.

Proposed publication schedule:

Deadline for submission of papers:

15 September 2009

Authors to receive a 1st decision by:

15 December 2009

Final notification of acceptance:

1 March 2010

On-line and print publication:

September 2010

Special Issue guest editors:

Marchal Pol, IMEC

E: marchal@imec.be

Yuan Xie

Pennsylvania State University

E: yuanxie@cse.psu.edu

IET Publishing Dept. contact:

Paul Rowley

(IET Computers & Digital Techniques Editorial Assistant)

E: prowley@theiet.org