

CALL FOR PAPERS

IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

February 20-21, 2003
Tampa/Clearwater, Florida, USA

<http://www.cse.psu.edu/~vijay/isvlsi03/>

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This symposium explores emerging trends and novel ideas and concepts in the area of VLSI. The symposium covers a range of topics: from VLSI circuits, systems, and design methods to system level design and system-on-chip issues, to bringing VLSI experience to new areas and technologies like nano- and molecular devices, MEMS, and quantum computing. Future design methodologies will also be one of the key topics at the workshop, as well as new CAD tools to support them. Over almost two decades the symposium has been an unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI. The symposium is bringing together leading scientists and researchers from academia and industry. The papers from this symposium have been published as the special issues of top archival journals. This fact indicates a very high quality of the symposium papers, and we are determined to keep a strong emphasis on this critical aspect of any conference. The symposium proceedings are published by IEEE Computer Society Press.

Several leading scientist from newly emerging areas of nanoelectronics, MEMS and molecular, biological and quantum computing will be invited speakers at the symposium. The symposium has established a reputation in bringing well-known international scientists as invited speakers, and this trend will continue.

Contributions are sought in the following areas:

- o Emerging Trends in VLSI
- o Nanoelectronics
- o Molecular, Biological and Quantum Computing
- o MEMS
- o VLSI Circuits and Systems
- o Field-programmable and Reconfigurable Systems
- o System Level Design
- o System-on-a-Chip Design
- o Application-Specific Low Power VLSI System Design
- o System Issues in Complexity, Low Power, Heat Dissipation
- o Power Awareness in VLSI Design
- o Test and Verification
- o Mixed-Signal Design and Analysis
- o Electrical/Packaging Co-Design
- o Physical Design
- o Intellectual property creating and sharing

The symposium program will include contributed papers and speakers invited by the Program Committee, as well as a poster session. The keynote addresses, panels and special sessions are planned as well.

PAPER SUBMISSION AND DEADLINES:

Authors should send a PDF version of their papers to the web based submission site, as described on the symposium website.

Submissions should be no more than ten pages long. The cover page should include (1) title, (2) authors and affiliation, (3) e-mail address of the contact author, (4) an abstract describing the contributions, and (5) body of the paper. Due dates are:

October 25, 2002 - Manuscript max ten pages
November 20, 2002 - Review results sent to authors
December 1, 2002 - Final paper for the Proceedings

This symposium will also have a poster session, and each accepted poster will appear as a short paper, at two pages in length, in the symposium proceedings. A PDF version of your short paper should be sent to the web based submission site, as described on the symposium website. The same due dates for short papers submission apply as for regular papers.